## **Patent Assignment Abstract of Title**

**Total Assignments: 1** 

Application #: 10085593 Filing Dt: 02/26/2002 Patent #: NONE Issue Dt:

PCT #: NONE Publication #: <u>US20030163445</u> Pub Dt: 08/28/2003

Inventors: Alpesh B. Oza, Miguel A. Guerrero

Title: Method and apparatus for high-speed address learning in sorted address tables

Assignment: 1

Reel/Frame: 012650/0056 Received: 03/12/2002 Recorded: 02/26/2002 Mailed: 05/03/2002 Pages: 3

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignors: OZA, ALPESH B. Exec Dt: 02/25/2002

GUERRERO, MIGUEL A. Exec Dt: 02/25/2002

**Assignee:** INTEL CORPORATION

2200 MISSION COLLEGE BOULEVARD SANTA CLARA, CALIFORNIA 95052

Correspondent: BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

MARK C. FARRELL

12400 WILSHIRE BOULEVARD

7TH FLOOR

LOS ANGELES, CA 90025

Search Results as of: 6/26/2004 6:52:03 P.M.

If you have any comments or questions concerning the data displayed, contact OPR / Assignments at 703-308-9723 Web interface last modified: Oct. 5, 2002